



MICROIP to Debut AI Vehicle System Business Group and Edge AI Innovations at COMPUTEX 2026

Descrizione

COMUNICATO STAMPA - CONTENUTO PROMOZIONALE

TAIPEI, May 31, 2026 /PRNewswire/ - MICROIP (TPEX: 7796), specializing in AI software and ASIC design services, will unveil its AI Vehicle System Business Group at COMPUTEX 2026 (Booth A1215a, TWTC Hall 1, June 2-5). Echoing the AI Together theme, the showcase focuses on edge AI, smart mobility, and cross-platform applications.

Emphasizing the concept of software driving hardware, Chairman Dr. James Yang noted that software design services will lead the edge AI market. He highlighted that the core challenge of AI implementation is the process fragmentation between software development workflows and underlying heterogeneous hardware, making deep integration the key to substantial commercial value.

Smart Automotive Safety & Dual AI Platforms The new AI Vehicle System Business Group will showcase flagship automotive hardware with automatic calibration. The system offers triple safety protection, including front-vehicle traffic light recognition, rear-vehicle blind spot warning, and in-cabin driver fatigue detection, alongside 360-degree multi-channel AI recognition. These solutions are ideal for smart fleet management and commercial safety monitoring.

MICROIP will also demonstrate its dual AI software platforms. AIVO, a no-code drag-and-drop vision platform, will showcase real-world applications for public transit monitoring, smart drones, and factory SOP inspections. To eliminate hardware limitations, the XEdgAI platform allows seamless deployment across mainstream chips like NVIDIA Jetson, Intel Core Ultra, and Axelera AI Metis, reducing vendor lock-in risks.

ASIC Design & Ecosystem Partnerships For chip design, MICROIP's CATS Customized ASIC Technology and Solutions integrates advanced AI EDA tools to assist R&D teams in shortening chip time-to-market by 6 to 9 months. At the event, MICROIP will offer a Free ASIC Design Technical Consulting Service, providing exclusive PPA optimization analysis to evaluate integration possibilities.

To propel the edge AI industry from chip to field deployment, MICROIP is expanding its global ecosystem. MICROIP partnered with industrial PC major AAEON to demonstrate real-time production line monitoring using the RT-621R rackmount chassis. Additionally, MICROIP partnered with neuromorphic AI chip pioneer BrainChip to develop low-power AI models and tactical edge radar classification technology.

About MICROIP MICROIP is a Taiwan-based leader in ASIC design services, AI software, and IP licensing. Through its CATS (Custom ASIC Technology & Solutions) and CAPS (Cross-platform AI Processing Service) platforms, it accelerates real-world AI deployment and shortens development cycles. Visit www.micro-ip.com.

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