



## Arasan announces the immediate availability of the industries first xSPI NOR + eMMC NAND Combo PHY IP

### Descrizione

COMUNICATO STAMPA â?? CONTENUTO PROMOZIONALE

NOR or NAND Flash? License AND! Arasan announces xSPI NOR AND eMMC NAND Combo PHY IP integrated seamlessly with its xSPI + eMMC Combo Controller IP

SAN JOSE, Calif., Jan. 29, 2026 /PRNewswire/ â?? Arasan Chip Systems, a leading provider of semiconductor IP for mobile and automobile SoCs, today announced the immediate availability of its xSPI + eMMC Combo PHY IP. This IP integrates both xSPI and eMMC 5.1 PHY into a single unified solution, enabling support for two distinct memory protocols within the same IP. The xSPI + eMMC Combo PHY IP is targeted at mission critical applications in defense and aerospace industries for SoCâ??s that require the reliability of the NOR Flash and bulk data storage capabilities and cost advantages of the NAND Flash. This IP can also be used by for life endangering applications like medical devices wherein reliability is paramount.

The xSPI + eMMC Combo PHY IP is engineered to address the growing demand for high-performance, area-efficient storage solutions in embedded and boot applications. By leveraging a shared I/O and analog front-end architecture, the design significantly reduces pin count and silicon footprint, delivering a cost-effective and power-optimized implementation for SoC and MCU platforms.



â??This dual-mode PHY allows customers to seamlessly support both eMMC and next-generation xSPI devices with a single low pin count IP, lowering system cost and accelerating time to market. With the launch of our xSPI + eMMC Combo PHY IP , Arasan continues to push the boundaries of storage interface IP innovation,â?• said Ron Mabry, VP of Sales at Arasan.

Arasan offers a comprehensive suite of mobile storage IP for solid state storage interfaces like UFS, eMMC, xSPI, NAND Flash and I2C Controller IP. Arasan has sold over 200 licenses of its eMMC IP and is also the leading provider xSPI IP thereby addressing the NOR and NAND Flash markets individually or how with this product, providing a combined solution.

The xSPI and eMMC Combo PHY IP is available to license immediately on leading foundries in nodes from 28nm to 3nm.

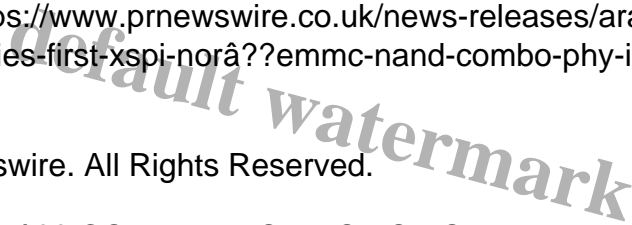



About Arasan:

Arasan Chip Systems is a leading provider of IP for mobile storage and mobile connectivity interfaces, with over a billion chips shipped with our IP. Our high-quality, silicon-proven Total IP Solutions encompass digital IP, Analog Mixed Signal PHY IP, Verification IP, HDK, and Software. With a strong focus on mobile SoCs, we have been at the forefront of the Mobile evolution since the mid-90s, supporting various mobile devices, including smartphones, automobiles, drones, and IoT devices, with our standards-based IP.

Photo    
[https://mma.prnewswire.com/media/2872016/xSPI\\_eMMC\\_Combo\\_PHY.jpg](https://mma.prnewswire.com/media/2872016/xSPI_eMMC_Combo_PHY.jpg)  
[https://mma.prnewswire.com/media/2724571/Arasan\\_Chip\\_Systems\\_Inc\\_Logo.jpg](https://mma.prnewswire.com/media/2724571/Arasan_Chip_Systems_Inc_Logo.jpg)

View original content:<https://www.prnewswire.co.uk/news-releases/arasan-announces-the-immediate-availability-of-the-industries-first-xspi-nor-eMMC-nand-combo-phy-ip-302673565.html>

Copyright 2026 PR Newswire. All Rights Reserved.

COMUNICATO STAMPA  CONTENUTO PROMOZIONALE: Immediapress  un servizio di diffusione di comunicati stampa in testo originale redatto direttamente dall'ente che lo emette.  Adn Kronos e Immediapress non sono responsabili per i contenuti dei comunicati trasmessi 

immediapress

## Categoria

1. Comunicati

## Tag

1. ImmediaPress

## Data di creazione

Gennaio 29, 2026

## Autore

redazione